



PATENT
0465-0758P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Jun S. LEE Conf.: 5244
Appl. No.: 09/722,583 Group: 2823
Filed: November 28, 2000 Examiner: B. KEBEDE
For: METHOD FOR FABRICATING CAPACITOR OF
SEMICONDUCTOR DEVICE

LARGE ENTITY TRANSMITTAL FORM

Assistant Commissioner for Patents
Washington, DC 20231

April 30, 2002

Sir:

Transmitted herewith is an amendment in the above-identified application.

- ☐ The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
- ☐ The enclosed document is being transmitted via facsimile.

The fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	10	-	20	=	0	\$18	\$0.00
INDEPENDENT	2	-	3	=	0	\$84	\$0.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						\$280	\$0.00
						TOTAL	\$0.00

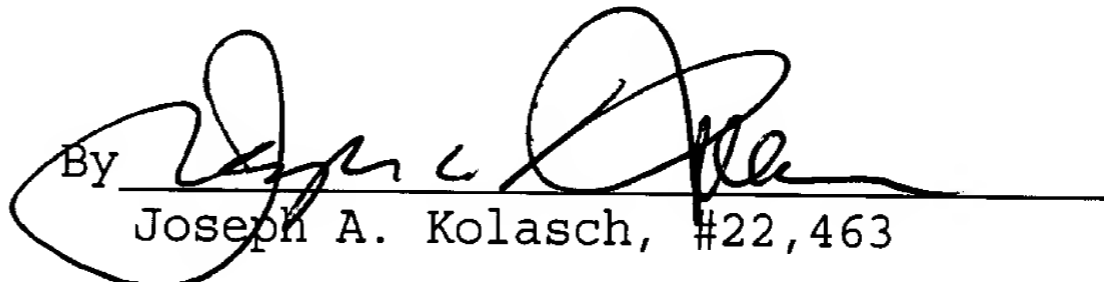
Appl. No. 09/722,583

- ☐ Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- ☒ No fee is required.
- ☐ Check(s) in the amount of \$0.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
Joseph A. Kolasch, #22,463

JAK/JES/abs
0465-0758P

P.O. Box 747
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ATTACHMENT

(Rev. 09/27/01)



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TECHNOLOGY CENTER 2800

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DRAWING CORRECTION AUTHORIZATION REQUEST

Assistant Commissioner for Patents
Washington, DC 20231

April 30, 2002

Sir:

Applicant respectfully requests the Examiner's authorization of the drawing corrections shown in red ink on the attached sheet(s) as follows:

Figures 1A-1E are amended to depict semiconductor substrate 10; and

Figures 2A-2E are amended to depict semiconductor substrate 110.

No new matter has been added by these changes. These drawing changes will be implemented into formal drawings by Applicant's bonded draftsman upon approval by the Examiner and subsequent allowance of the application.

Appl. No. 09/722,583

If necessary, the Commissioner is hereby authorized in this, concurrent, and further replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 

Joseph A. Kolasch, #22,463

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Attachments:

(Rev. 11/09/01)